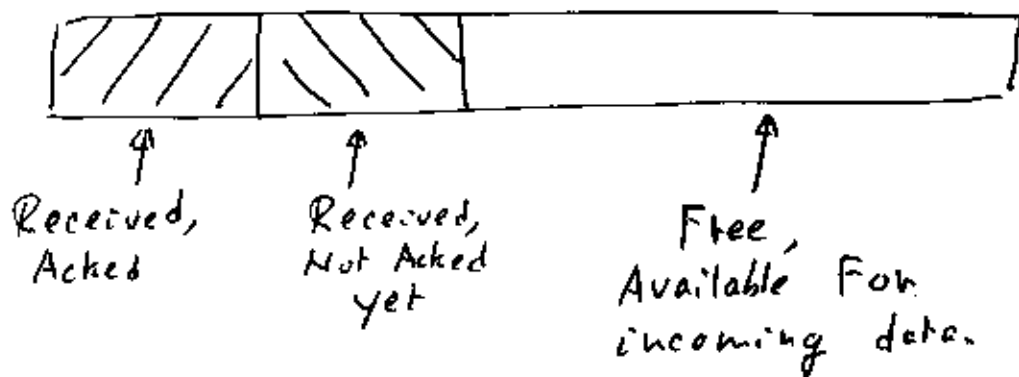


# Goal of Sliding Window Protocols.

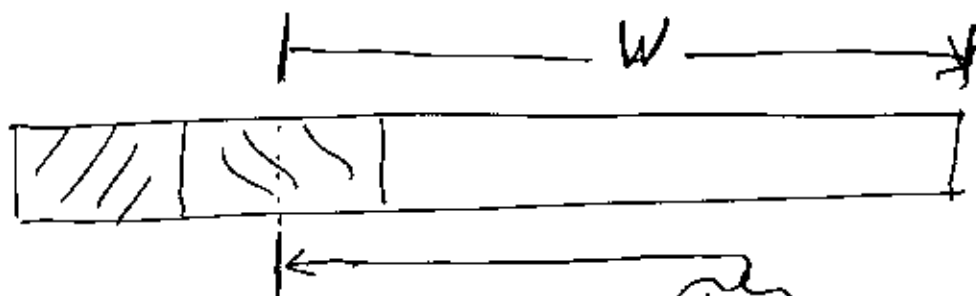
- (1) Flow Control. ←
- (2) Error Control  
(incl. missing, damaged, frames).

At Receiver Side:

Receive Buffer:



The receiver can choose to delay sending an ACK until it has enough space in its Receive Buffer to get more.



Wise to Ack only up to here

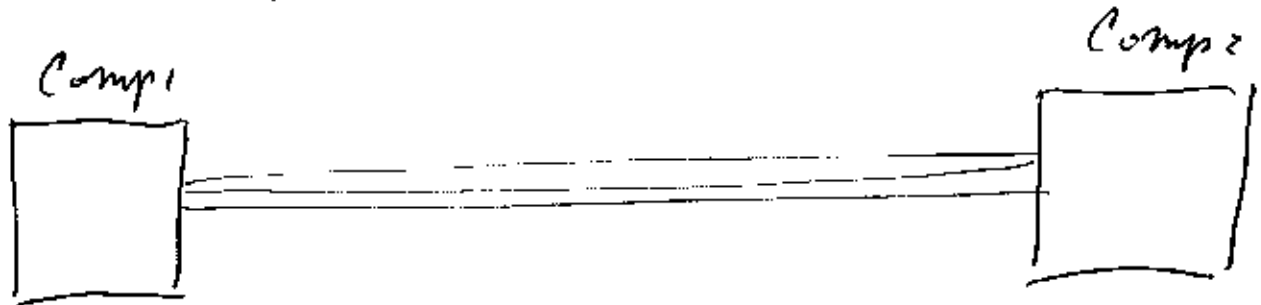
~~10/23/2023~~

X.25: (Vanerbaum p 61)

First public data network.

Computer to computer over phone line.

Computer places call. 64 kb/sec



Multiple connections inside one phone call.

Packets (Frame) 3 Byte Header.  
Up to 128 Bytes of data.

Header: 12 bit connection identifier.  
packet sequence number  
acknowledgement number.  
etc. Used flow control, Error Control.

Bell System had its own variation:

BX.25 ~ 1970-1985?

Error Control, Flow Control

"Not very compatible with Internet".

Because of the small X.25 frame,  
an IP packet had to be distributed  
over several X.25 frames.

(IP fragmentation).

See CIS 6456  
CIS 656

Flexible, not nice.

---

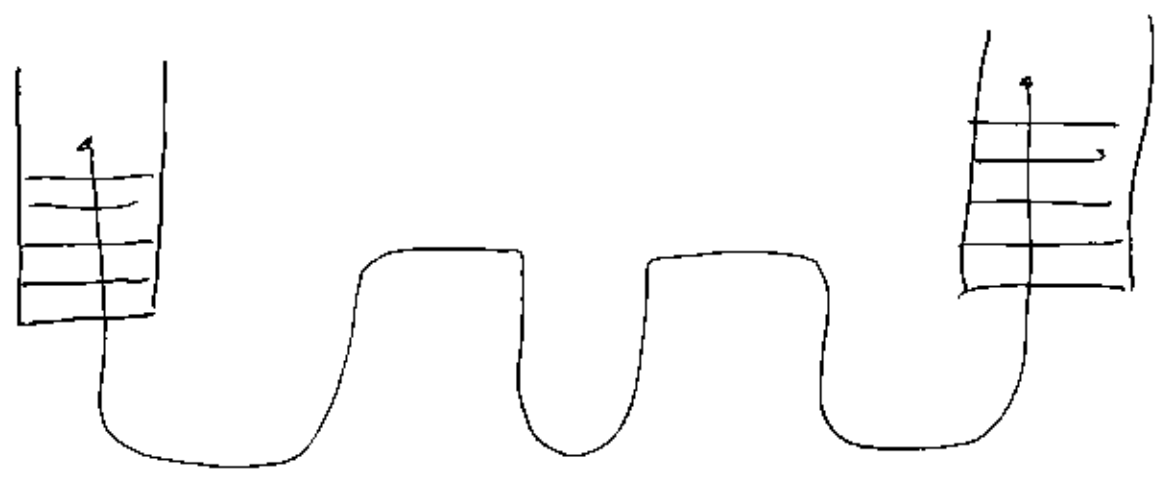
X.25 had own higher layers:  
not very compatible with IP.

---

Frame Relay. (1985? - ?) (still in use?)  
Tanenbaum p 61

Similar to X.25,  
No <sup>Has</sup> error detection (correction)  
No error control  
No flow control. No Re-transmission  
Connection oriented. (in-ordered delivery)  
Inter connecting LANs

Why no ... control ?



Links: Highly Reliable.  
error control needed anyhow.  
Lower Layer controls not needed.

Frame Relay:

Max. Frame Size is 9000 Bytes.

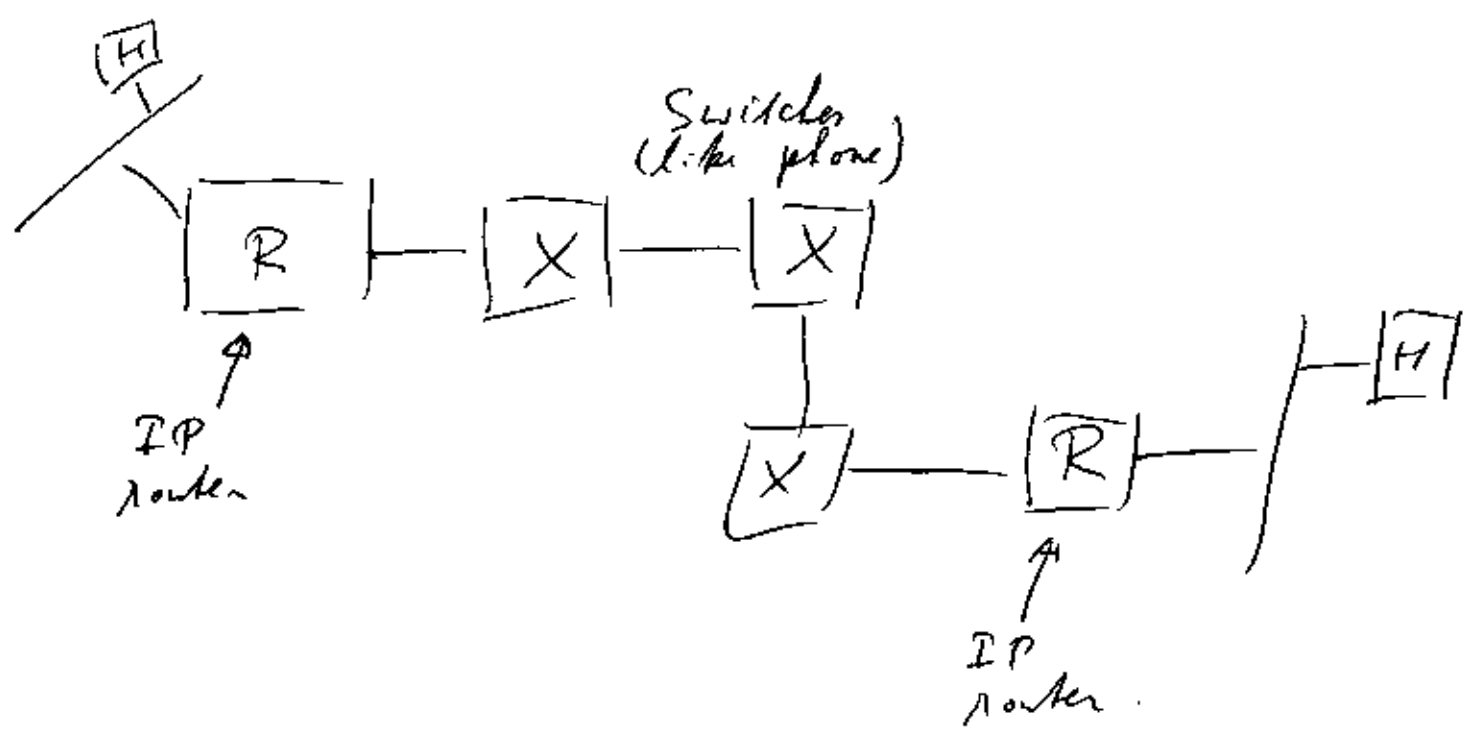
Much less need for IP fragmentation.

(in 1985: practically no need).

Frame Relay: Strictly layer 2.

Compatible with OSI,  
IP.

Frame Relay used Virtual Circuits,  
cell set-up.



# ATM.

A synchronous Transfer Mode.  
(Automatic Teller Machine).

Big Hype.

Small Flop.

Would do everything for  
every body.

---

Now; Layer 2 only (?)

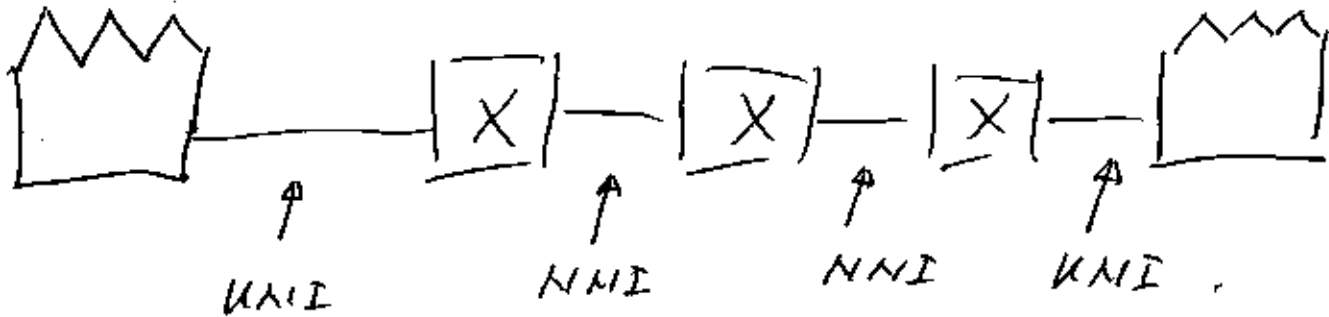
Fixed <sup>Frame</sup> ~~packet~~ size,  
called cells.

48 data Bytes

5 header Bytes.

UNI : User- Network Interface

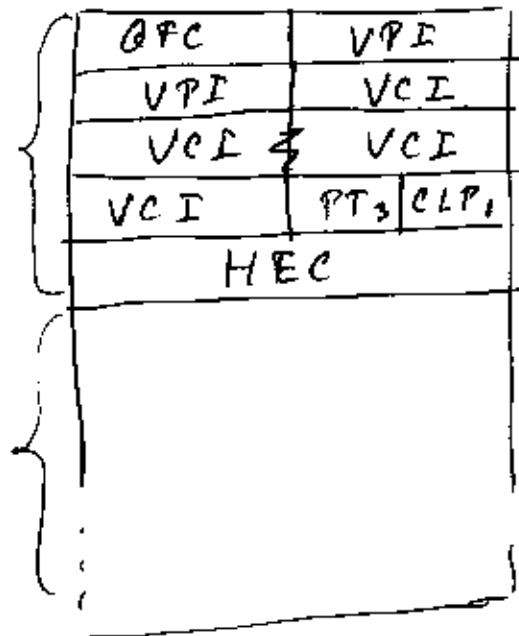
NNI : Network- Network Interface.



UNI :

5 Bytes

48 Bytes



GFC :

Generic Flow Control

VPI : Virtual Path

Identification

VCI : Virtual Circuit Identification

PT : Payload Type (3 bits)

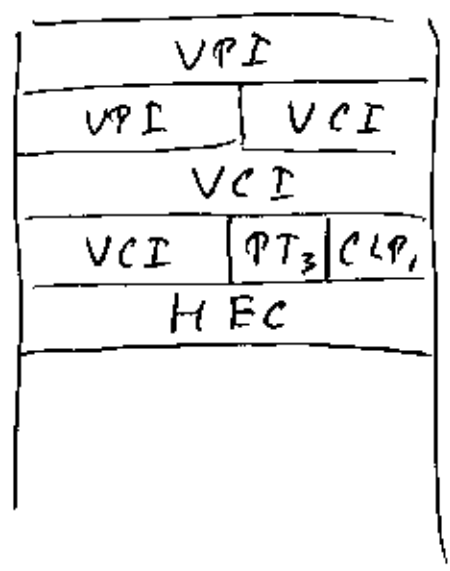
CLP : Cell Loss Priority (1 bit)

HEC : Header error control. (or CRC).

$$2^8 = 256 \text{ VPIs}$$

$$2^{16} = 65536 \text{ VCIs}$$

NNI

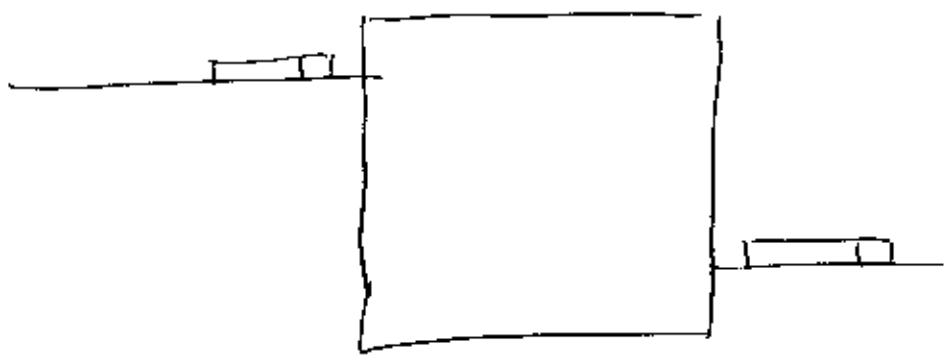


$$2^{12} = 4096 \text{ VPIs}$$

$$2^{16} = 65536 \text{ VCIs}$$



ATM  
Switch

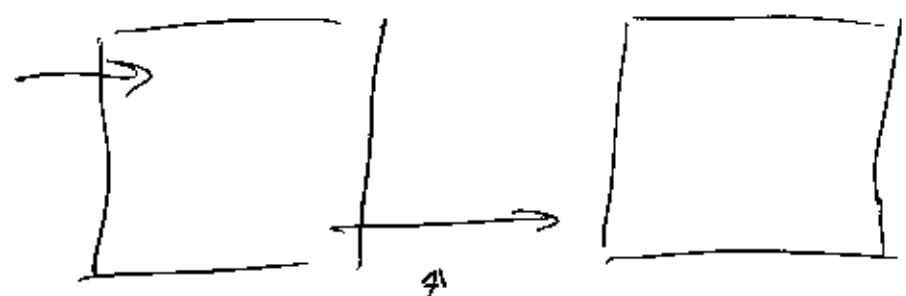


ATM input port:  
 Read VPI.  
 Translate into output port,  
 new VPI.

Why? Cell Set-up.

Why?

cell set-up



A VPI must be available

AA2.

ATM Application Layer.

There ~~are~~ <sup>were</sup> 5 of them.

Now only 4:

- AA1
  - AA2
  - AA3/4
  - AA5
- } for IP.

IP packet over ATM:

The IP packet ~~is~~ usually is

>> 48 bytes:

Must be broken in pieces.

SAR Segmentation and Re-assembly.

(maybe later?)

ATM cell size:

48 data ~~cell~~ bytes.

Fight in standards committee:

Europeans: 32 bytes

Americans: 64 bytes.

Compromise: 48 Bytes.

(worst possible?)

ATM: connection oriented.

~~The~~ product of PTTs.

Heavy weight.

---

Usual Mode:

IP over ATM over SONET (over optical fiber).

Moving toward:

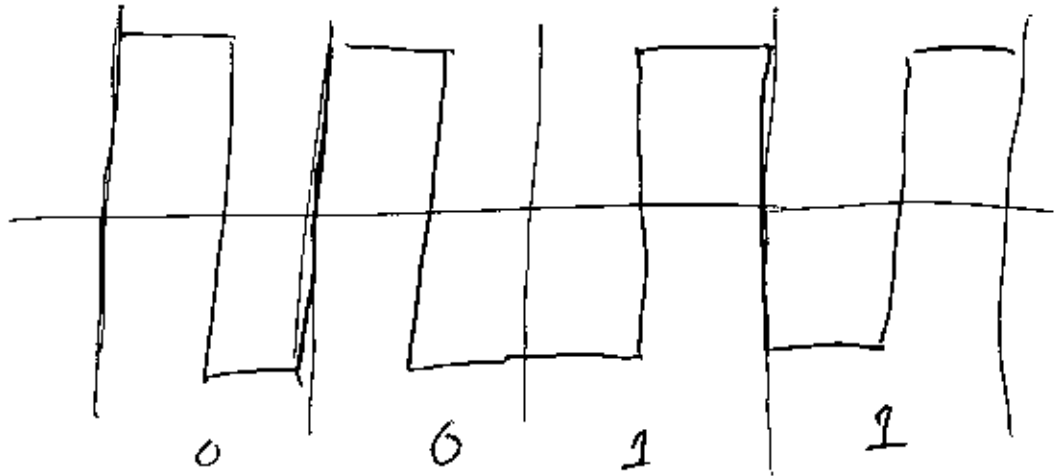
IP over optics.

More on Modulation.

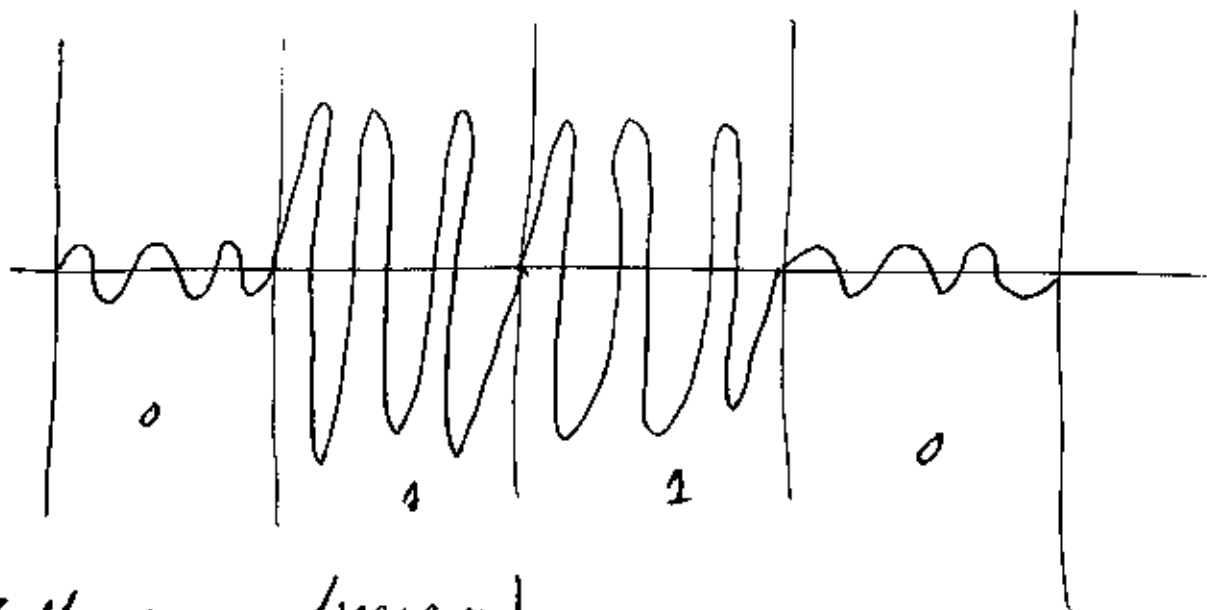
(Twisted Wire, Coax).

① Base band.

e.g. Manchester Encoding

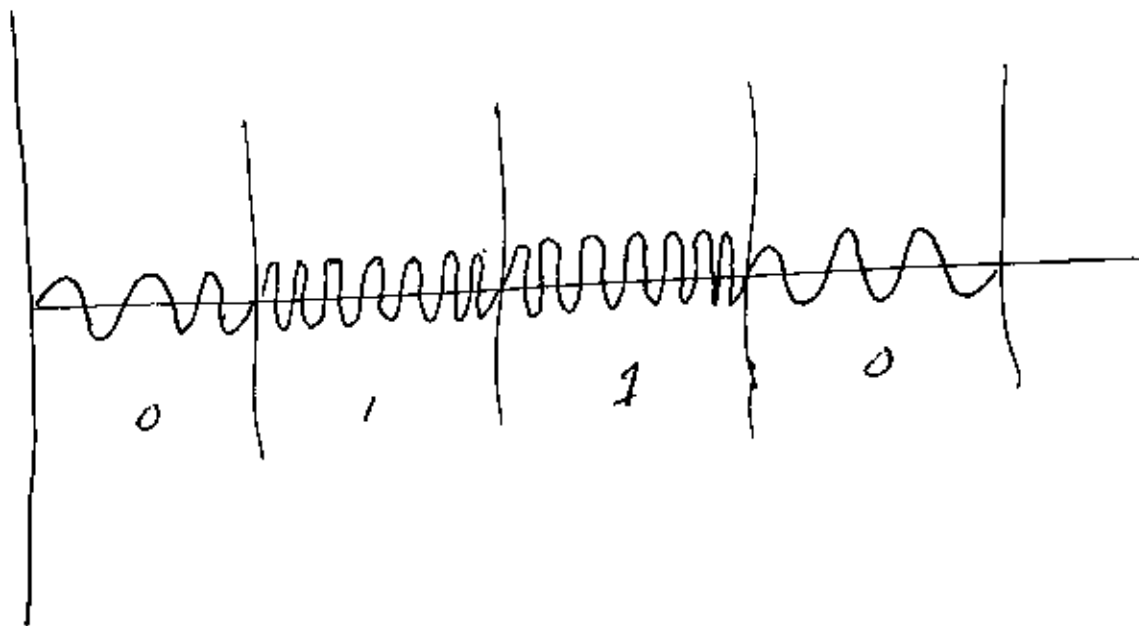


(2) Amplitude Modulation

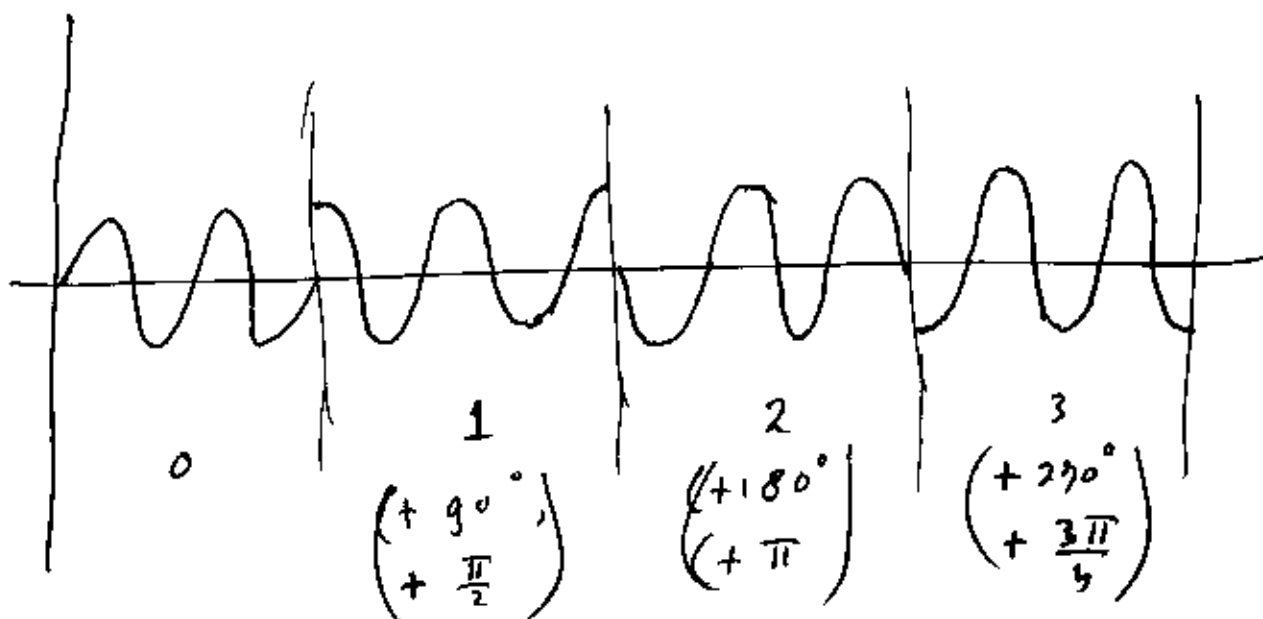


(all same frequency)

3 Frequency Modulation



## 4 Phase Modulation



4 symbols per

4 possibilities per symbol.

2 bits per band.

---

Difference between band rate

and bit rate.

Phase Modulation.

The method we just saw is called QPSK.

Quadrature Phase Shift Keying.

We did it with 4 <sup>(possible)</sup> symbols per band:  
2 bits per band.

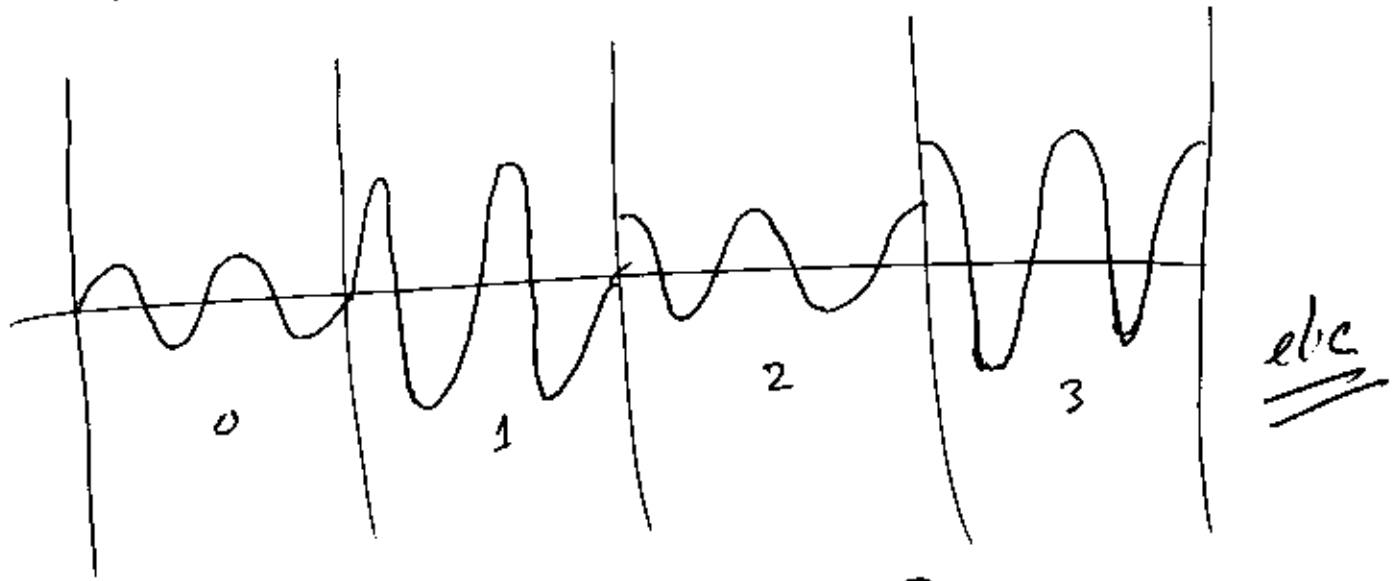
band rate: # "samples" per second.

bit rate: # bits/sec.

with  $2^k$  different phases:  $k$  bits/band.

(5) QAM.

Quadrature Amplitude Modulation.

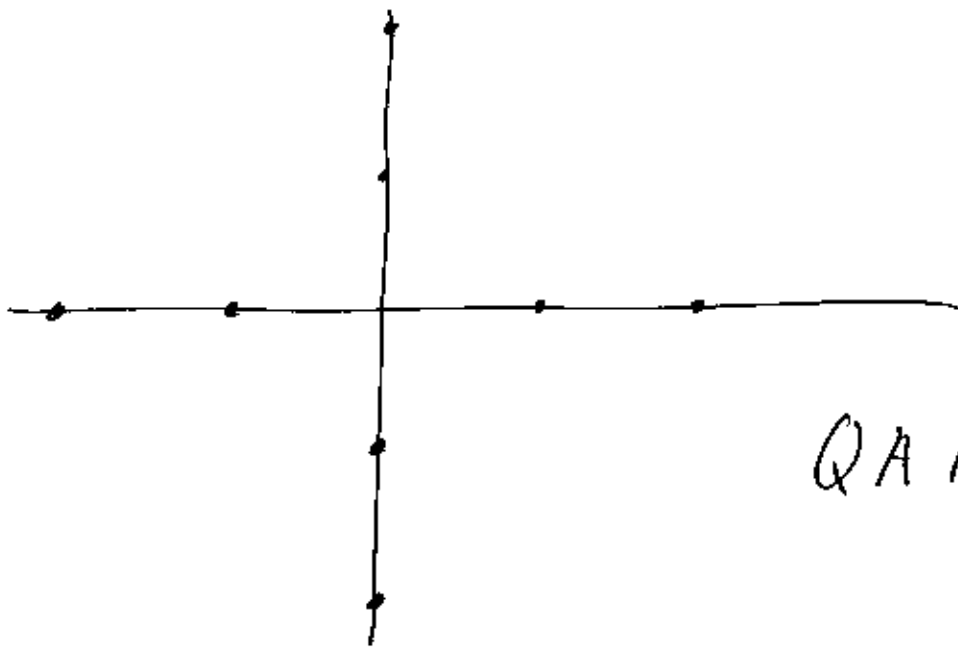


2 possible amplitudes }  
 4 possible phases }

} 8 possible symbols.  
 3 bits per baud.

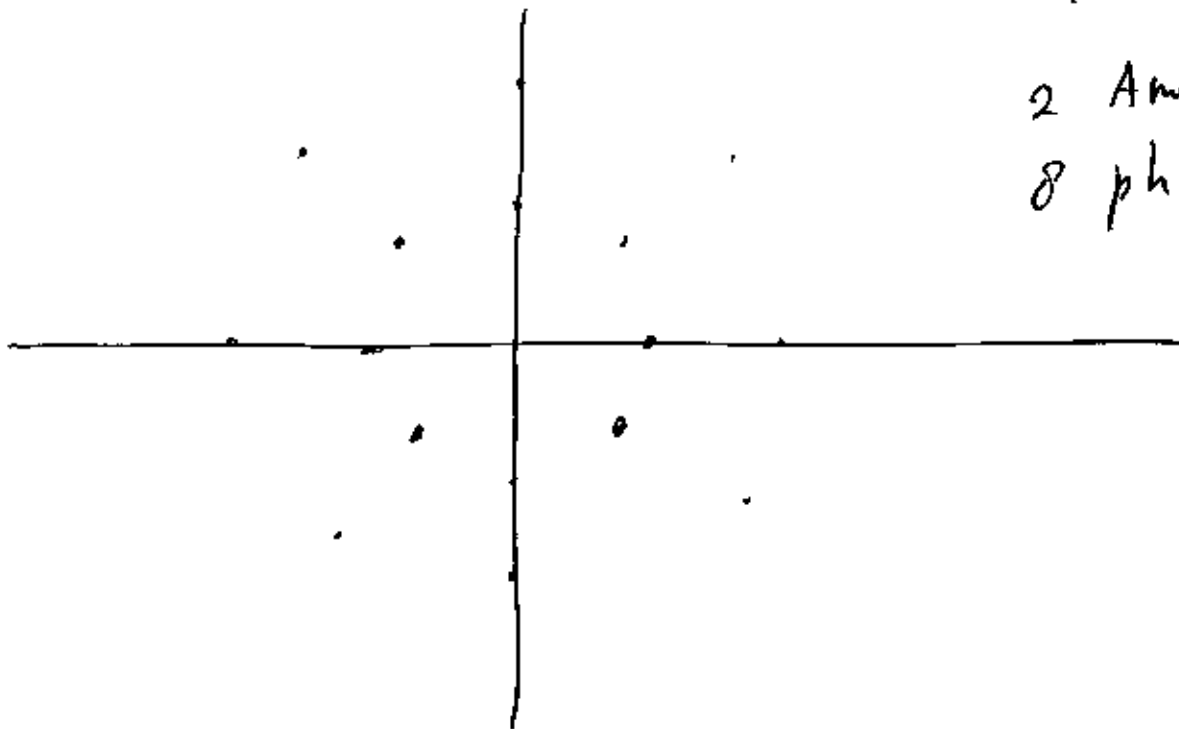
QAM-8.



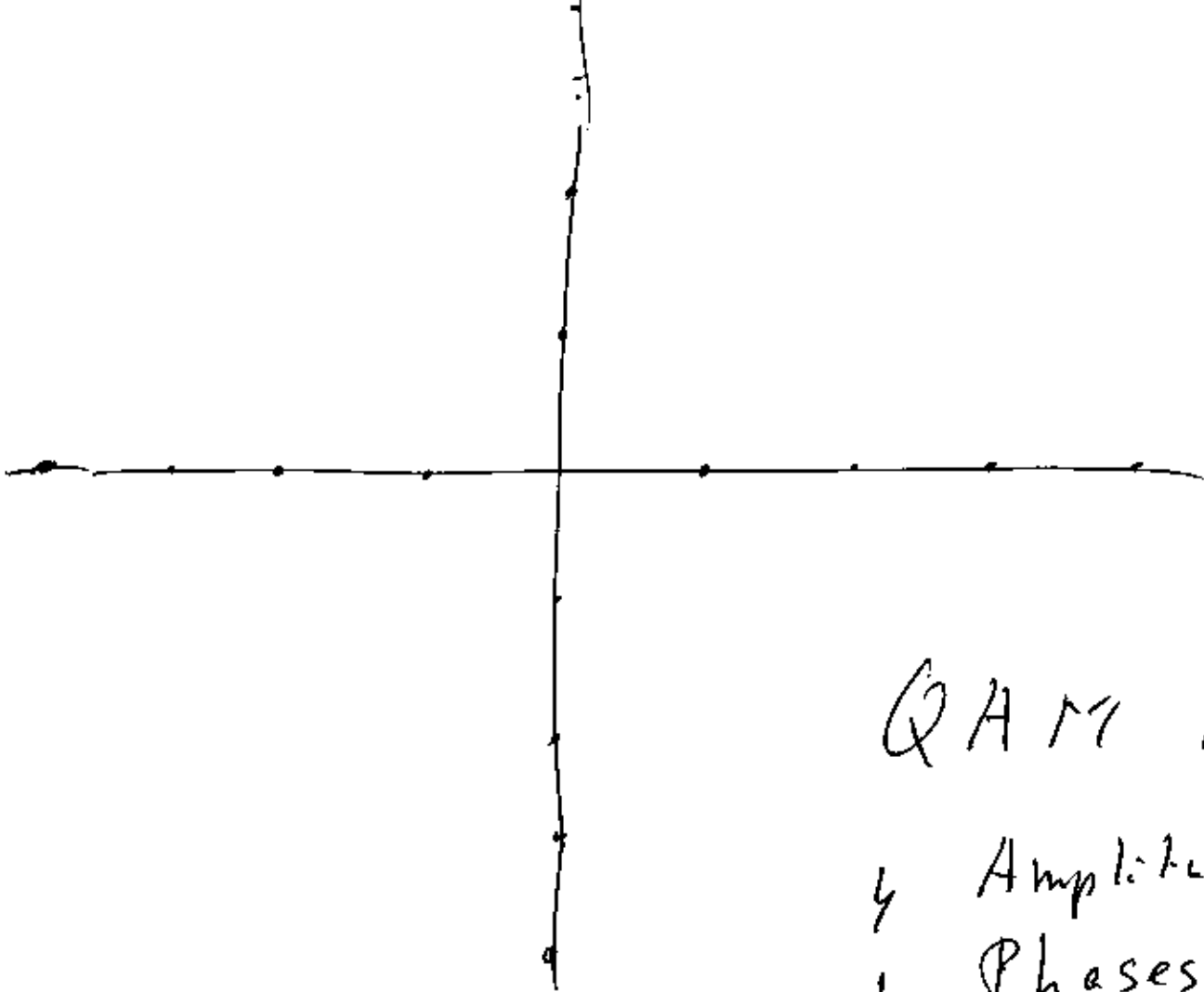


QAM-8.

Tanenbaum's pictures (p 128 etc)  
are wrong.



QAM-16  
2 Amplitudes,  
8 phases



QAM 16.

4 Amplitudes

4 Phases.

There is another QAM 16,

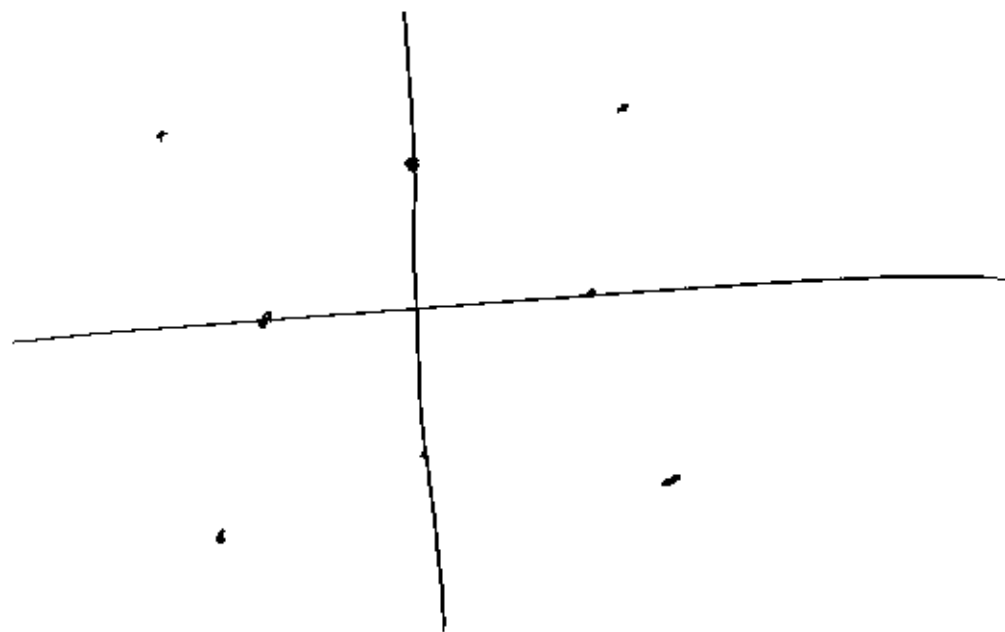
See Tanenbaum p 128 Fig. 2-25-b.

However, Tanenbaum is wrong: that one has 12 phases and 3 Amplitudes.

(NOT 4 and 4, as Tanenbaum says on page 127.) Some people might call this a Trellis Code.

# Trellis Codes

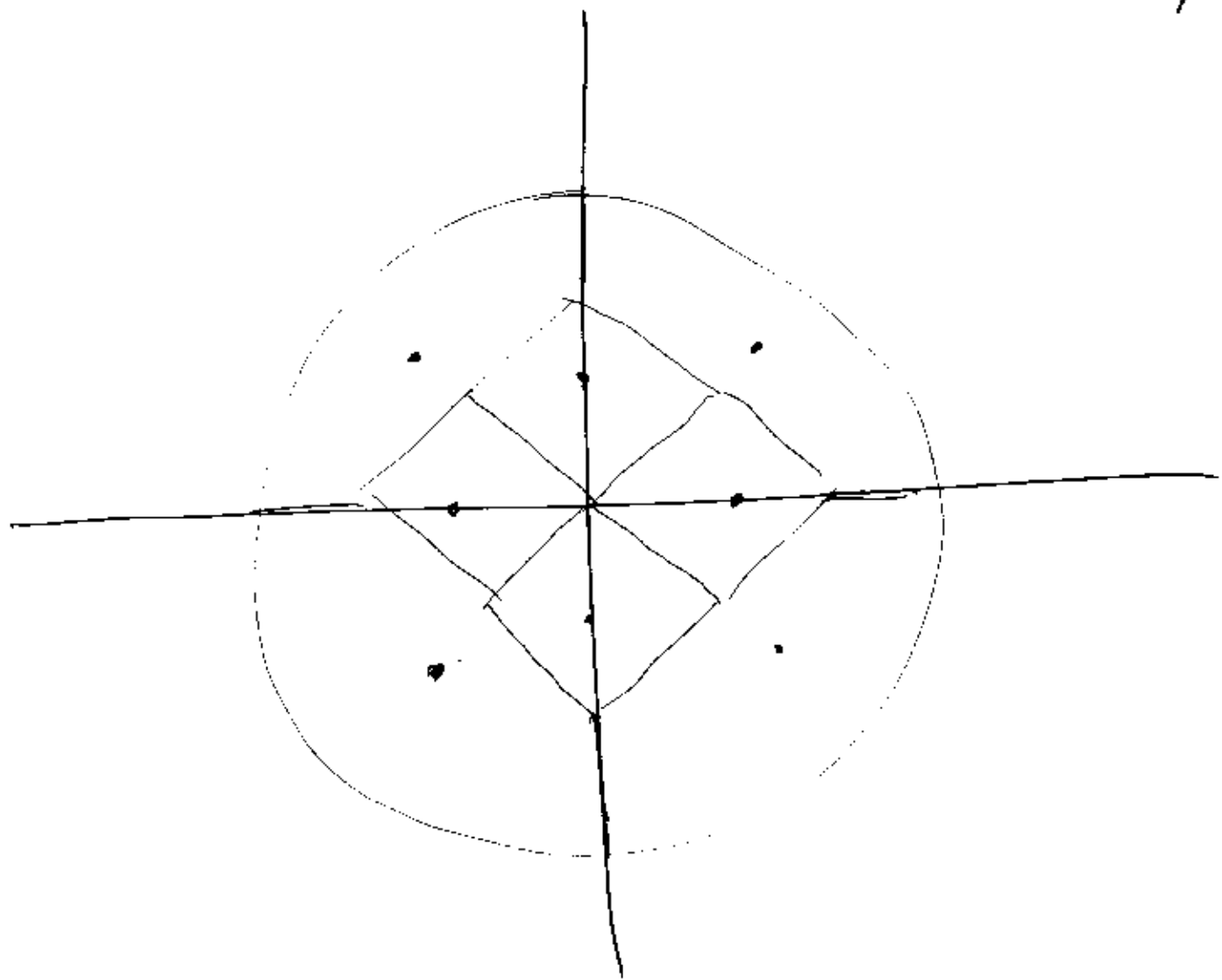
## TCM Trellis Coded Modulation



8 points : 3 bits / baud.

Note : "so many amplitudes,  
so many phases".

### Vector Quantization



Design of Trellis Codes:

"Maximize # points subject to minimal distance"  
 ("maximize minimal distance").

This one has 8 phases,  
 2 amplitudes,  
 but only 8 points, not 16.  
 (8 symbols / baud).

Trellis Codes:

Plus "Forbidden Regions".

(IF you get a symbol in that  
area: error).

Like a parity bit.

---

We already saw:

Error detecting codes.

(IP checksum,  
CRC in ethernet, ATM).

Now:

Error Correcting Codes.

The ultimate:

Reed - Solomon. (1960).

(etc.)  
Research still going strong!

Tough Math.

Used in CDs.

Used by Space Probes.

Error correcting codes: only way  
of getting close to Shannon bounds.

# Error Correcting Codes:

Send  $m$  message bits  
 $h$  redundant bits  
 (check bits),

Such, that as long as at least  
 $k$  bits are Ok,  
 (arrive Ok),

We can reconstruct the whole  
 message. (Some specific  $k$ ,  $m < k < m+h$ ).

IF:

$P\{\text{at least } k \text{ bits Ok}\} \sim 1.0$   
 practically 1.

Data rate:  $\sim m$  bits / ~~sec~~ codeword.

With  $C$  codewords/sec:

$$\text{Data Rate} = (m * C) \text{ bits/sec}$$

(send  $m+h$ , get  $m$ ).

IF  $k = m + r - 1$  :

we can correct single bit errors.

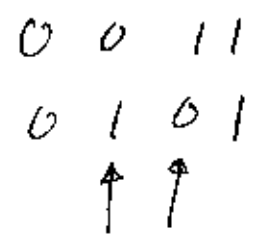




# Nearest: Hamming Distance

# bits different.

$$H((0,0,1,1), (0,1,0,1)) = 2$$



Code word: length  $m+k$   
 $\binom{m+k}{2}$  total number codewords.

of these:  $2^m$  are legal  
all others illegal :  $2^{m+k} - 2^m = 2^m(2^k - 1)$ .

If we ~~find~~ receive an illegal codeword:  
As long as there is a unique closest  
legal codeword: that is the one.

Not necessarily: "some bits are message, some are check".

Other example:  
 "n x n" code word

Parity  
 ↓ (even parity).

$d_{1,1}$	...	$d_{1,n}$	$\Phi_{1,n+1}$
$d_{2,1}$	...	$d_{2,n}$	$\Phi_{2,n+1}$
⋮		⋮	⋮
$d_{n,1}$	...	$d_{n,n}$	$\Phi_{n,n+1}$
P →		$\Phi_{n+1,1}$	...
		$\Phi_{n+1,n}$	$\Phi_{n+1,n+1}$

$m = n \times n = n^2$  message bits.

$n = 2n+1$  check bits.

As long as among  $(n+1)^2$  bits at most  
 one <sup>bit</sup> error: error can be corrected.

(One row, One column.  
 Can be  $(N+1)$ -th row and/or column).

Other example:

183

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Hamming Codes.

Tanenbaum, pp 192-195.

$m$  message bits  
 $r$  check bits.

As long as

$$2^r - r - 1 \geq m$$

it is possible to have a code with  $2^m$  legal code words,

$$2^{m+r} - 2^m = 2^m (2^r - 1) \text{ illegal codewords,}$$

and that corrects all ~~single~~ single bit errors.

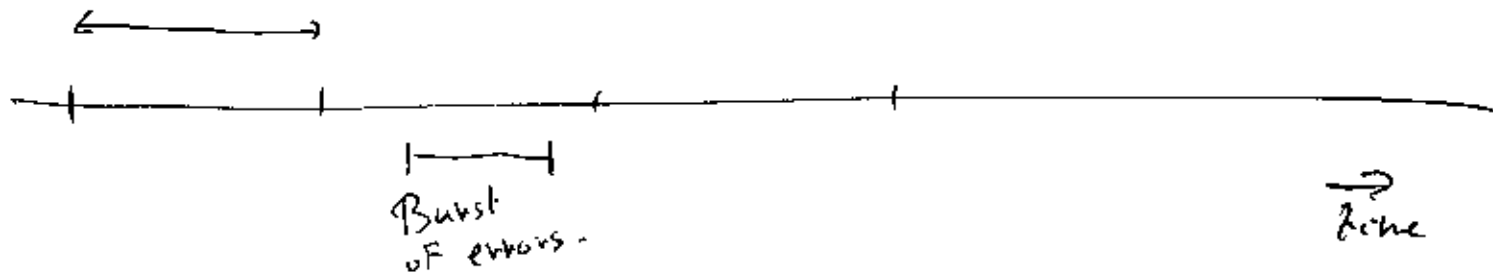
In real life we are not interested in correcting single bit errors.

Errors come in bursts.

one  
codeword.

185

~~184~~



Suppose one codeword contains  
 $m$  message bits,  
 $r$  check bits  
can correct up to  $k$  errors.  
(out of  $m+r$  bits).

Errors: bursty.

High probability: No error in codeword.

Small prob:  $> k$  errors

Negligible prob:  $0 < \# \text{ errors} < k$ .

Error Correcting Code is useless!

Solution: Make codewords long.

---

Reed Solomon: Used in CDs,  
Space Probes,  
...

For Real-Time Traffic:

186 ~~183~~

Long Code words introduce delay.  
(“Like” packetization delay).

---

Related to Error Correcting Codes:  
RAID.

Redundant Array of  
Inexpensive Disks.  
Tanenbaum p 308.



$M$  disks.

$(M+1)$ -th : contains “bit by bit”  
“parity bit”. (Simplified).

As long as at most one of the  
 $M+1$  disks breaks: content can be  
re-constructed from other  $M$ .

With Reed-Solomon:

187 ~~186~~

As long as at most  $k$  out  
of  $M+R$  disks break,

Content can be reconstructed.

(  $k \leq R$  ).